



Form PTO-1449 (Rev. 6-98)

U.S. Department of Commerce  
Patent and Trademark OfficeAttorney Docket No.  
48452-047Serial No.  
09/703,884INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

Applicant

Robert S. Phillips

Filing Date

November 1, 2000

Group Art Unit

2151

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number							Date	Name	Class	Sub Class
ET	4	0	9	6	5	6	7	6/20/78	Millard et al.		
	4	4	2	6	6	8	8	1/17/84	Moxley		
	4	7	1	0	9	2	6	12/1/87	Brown et al.		
	4	8	6	8	7	3	8	9/19/89	Kish et al.		
	4	9	1	4	5	7	1	4/3/90	Baratz et al.		
	4	9	3	4	7	6	4	6/19/90	Leitermann et al.		
	5	0	5	5	9	9	9	10/8/91	Frank et al.		
	5	0	6	7	0	7	1	11/19/91	Schanin et al.		
	5	0	7	7	7	3	6	12/31/91	Dunphy, Jr., et al.		
	5	1	1	7	3	5	0	5/26/92	Parrish et al.		
	5	1	1	9	4	8	1	6/2/92	Frank et al.		
	5	2	2	6	0	3	9	7/6/93	Frank et al.		
	5	2	3	7	6	7	3	8/17/93	Orbits et al.		
	5	2	4	5	5	6	3	9/14/93	Hauck, Jr.		
	5	2	4	7	6	7	3	9/21/93	Costa et al.		
	5	2	5	1	3	0	8	10/5/93	Frank et al.		
ET	5	2	6	9	0	1	3	12/7/93	Abramson et al.		

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FT	5	2	7	4	7	8	9	12/28/93	Costa et al.	RECEIVED	
FT	5	2	7	6	8	5	2	1/4/94	Callander et al.	OCT 25 2004	
	5	2	8	2	2	0	1	1/25/94	Frank et al.	Technology Center 2100	
	5	2	9	7	2	6	5	3/22/94	Frank et al.		
	5	2	9	7	2	6	9	3/22/94	Donaldson et al.		
	5	3	0	3	3	6	2	4/12/94	Butts, Jr. et al.		
	5	3	0	7	5	0	6	4/26/94	Colwell et al.		
	5	3	1	3	6	4	7	5/17/94	Kaufman et al.		
	5	3	3	5	3	2	5	8/2/94	Frank et al.		
	5	3	4	1	4	8	3	8/23/94	Frank et al.		
	5	3	9	0	3	1	6	2/14/95	Cramer et al.		
	5	3	9	0	3	2	6	2/14/95	Shah		
	5	3	9	4	5	5	5	2/28/95	Hunter et al.		
	5	4	0	8	6	4	9	4/18/95	Beshears et al.		
	5	4	2	3	0	3	7	6/6/95	Hvasshovd		
	5	4	5	2	4	4	7	9/19/95	Nelson et al.		
	5	4	8	3	6	5	2	1/9/96	Sudama et al.		
	5	4	8	5	4	5	5	1/16/96	Dobbins et al.		
FT	5	4	9	0	2	7	2	2/6/96	Mathias et al.		

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<i>FS</i>	5	4	9	3	7	2	8	2/20/96	Solton et al.	—	—
<i>FS</i>	5	5	1	3	3	1	4	4/30/96	Kandasamy et al.	—	—
<i>FS</i>	5	5	1	9	8	5	5	5/21/96	Neeman et al.	—	—
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<i>FS</i>	5	5	5	1	0	3	5	8/27/96	Arnold et al.	—	—
<i>FS</i>	5	5	6	0	0	2	7	9/24/96	Watson et al.	—	—
<i>FS</i>	5	5	6	0	0	2	9	9/24/96	Papadopoulos	—	—
<i>FS</i>	5	5	8	8	1	4	7	12/24/96	Neeman et al.	—	—
<i>FS</i>	5	5	9	2	6	2	5	1/7/97	Sandberg	—	—
<i>FS</i>	5	6	0	8	9	0	3	3/4/97	Prasad et al.	—	—
<i>FS</i>	5	6	1	3	0	7	9	3/18/97	Debique	—	—
<i>FS</i>	5	6	1	7	5	3	7	4/1/97	Yamada et al.	—	—
<i>FS</i>	5	6	3	4	0	0	4	5/27/97	Gopinath et al.	—	—
<i>FS</i>	5	6	4	9	1	9	4	7/15/97	Miller et al.	—	—
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<i>FS</i>	5	6	8	7	3	0	8	11/11/97	Jardine et al.	—	—
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<i>FS</i>	5	6	9	2	1	8	3	11/25/97	Hapner et al.	—	—

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Examiner Initial	Document Number							Date	Name	Class	Sub Class
PT	5	6	9	2	1	9	3	11/25/97	Jagannathan et al.	RECEIVED OCT 20 2004 Technology Center 2100	
↑	5	6	9	6	9	0	0	12/9/97	Nishigaya et al.		
	5	7	0	1	4	6	2	12/23/97	Whitney et al.		
	5	7	2	7	1	5	0	3/10/98	Laudon et al.		
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	5	8	0	5	7	8	6	9/8/98	Badovinatz et al.		
	5	8	1	2	5	3	3	9/22/98	Cox et al.		
	5	8	1	2	7	5	1	9/22/98	Ekrot et al.		
	5	8	1	5	7	1	0	9/29/98	Martin et al.		
	5	8	3	2	5	1	4	11/3/98	Norin et al.		
	5	8	4	8	4	1	9	12/8/98	Hapner et al.		
	5	9	3	3	8	2	6	8/3/99	Ferguson et al.		
	6	1	1	5	5	4	9	9/5/00	Janis et al.		
PT	6	1	4	8	3	7	7	11/14/00	Carter et al.		

Examiner

Frank

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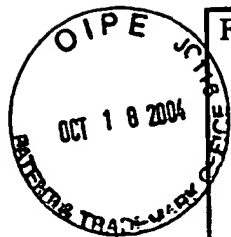
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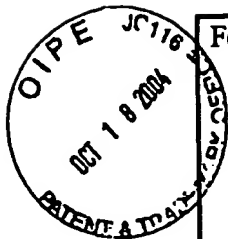
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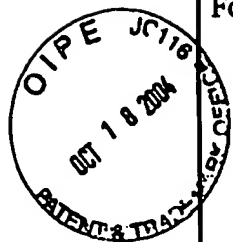


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U.S. Department of Commerce Patent and Trademark Office				48452-047		09/703,884	
<b>INFORMATION DISCLOSURE STATEMENT</b> (Use several sheets if necessary)				Applicant <b>Robert S. Phillips</b>			
				Filing Date November 1, 2000		Group Art Unit 2151	
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial	Document Number	Date	Country	Class	Sub Class	Translation	
						Yes	No
FT	0 661 651 A1	7/5/95	EPO	—	—	Y	
↑	0 288 636 A2	11/2/88	EPO	—	—	Y	
	2 257 273 A	1/6/93	GB	—	—	Y	
	96/30847	10/3/96	WO	—	—	Y	
	96/23268	8/1/96	WO	—	—	Y	
	95/25306	9/21/95	WO	—	—	Y	
	95/22111	8/17/95	WO	—	—	Y	
↓	95/14279	5/26/95	WO	—	—	Y	
FT	95/02307	1/19/95	WO	—	—	Y	
Examiner <i>Frank</i>			Date Considered 1/21/05				
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U.S. Department of Commerce Patent and Trademark Office Technology Center		210018452-047	09/703,884	
<b>INFORMATION DISCLOSURE STATEMENT</b> (Use several sheets if necessary)		Applicant <b>Robert S. Phillips</b>		
		Filing Date <b>November 1, 2000</b>	Group Art Unit <b>2151</b>	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
FT	"An Argument for Simple COMA," Ashely Saulsbury et al., <i>Proceedings of the First Annual Symposium on High Performance Computer Architecture</i> , Jan. 1995, pp. 276-285			
↑	"Design of the Trucluster Multicomputer System for the Digital UNIX Environment," Wayne M. Cardoza et al., <i>Digital Technical Journal</i> , Vol. 8, No. 1 (1996) pp. 5-17			
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	"SiCO: A Simple COMA Implementation," Ashely Saulsbury et al., Swedish Institute of Computer Science Technical report (undated)			
	"Software Agents," Genesereth et al., <i>Communications of the ACM</i> , July 1994, vol. 37, No. 7, pages 48-53.			
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	"Synapse Tightly Coupled Multiprocessors: A New Approach to Solve Old Problems," Steven J. Frank et al., <i>AFIPS National Computer Conference Proceedings</i> , July 1984, pp. 41-50			
	"The KSR1: Bringing the Gap Between Shared Memory and MPP's," Steven J. Frank et al., <i>IEEE Digest of Papers- Compcon '93</i> , Feb. 1993, pp. 285-294.			
	"Tightly Coupled Multiprocessors System Speeds Memory-Access Times," Steven J. Frank et al., <i>Electronics</i> , Vol. 57, no. 1, Jan. 12, 1984, pp/ 164-169			
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	Anderson et al. "Scheduler Activations: Effective Kernel Support for the User-Level Management of Parallelism," <i>Operating Systems Review (SIGOPS)</i> , Vol. 25, No. 1, January 1, 1991, pgs. 95-109.			
1	Anderson, T., et al. "Serverless Network File Systems," <i>Operating Systems Review (SIGOPS)</i> , Vol. 29, No. 5, December 1, 1995, pgs. 109-126.			
FT	Barton-Davis et al. "Adding Scheduler Activations to Mach 3," <i>Proceedings of the USENIX Mach III Symposium</i> , Santa FE, NM, USA, April 19-21, 1993, pgs. 119-136.			
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Frank		1/21/05		
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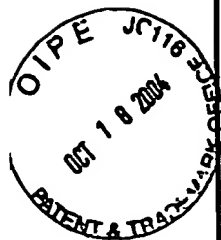
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

FT	Carter, J., et al. "PPFS: A High Performance Portable Parallel File System," Proceeding of the International Conference on Supercomputing, Barcelona, July 3-7, 1995, pgs. 385-394.
↑	Carter, J.B. et al., "Distributed Operating Systems Based on a Protected Global Virtual Address Space," In the Proceedings of the Third Workshop on Workstation Operating Systems (WWOS) (May, 1992)
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	European Patent Office, European Search Report, Application No. EP 97 30 9471, mailed on April 23, 1998, 3 pages.
	Henskens et al. "Course and Fine Grain Objects in a Distributed Persistent Store," <i>Object Orientation in Operating Systems</i> , 1993, IEEE, pgs. 116-123.
FT	Irelenbusch et al. "Towards a Resilient Shared Memory Concept for Distributed Persistent Object Systems," <i>Proceeding of the 28<sup>th</sup> Annual Hawaii Intl. Conference on System Sciences</i> , 1995, IEEE, pgs. 675-684.
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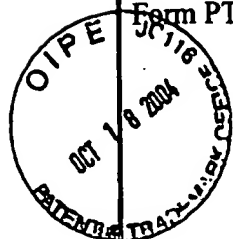
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			Filing Date November 1, 2000	Group Art Unit 2151
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
FT	Koppe C. "Sleeping Threads: A Kernel Mechanism for Support of Efficient User Level Threads," <i>Proceedings of Seventh IASTED-ISMM International Conference on Parallel and Distribution Computing and Systems</i> , Washington, DC, USA, October 19-21, 1995, pgs. 11-15.			
↑	Lea et al. "COOL: System Support for Distributed Programming," <i>Communications of the ACM</i> , Vol. 36, No. 9, pgs. 37-46.			
↑	Lee, E., et al. "Petal: Distributed Virtual Disks," 7 <sup>th</sup> International Conference on Architectural Support for Programming Languages and Operation Systems, Cambridge, MA, October 1-5, 1996, pgs. 84-92.			
↑	Li, Kai, "Shared Virtual Memory on Loosely Coupled Multiprocessors," Yale University, Department of Computer Science, (Sept, 1986).			
↑	Measurement Techniques Inc. Web Page, <a href="http://www.lancache.com/pr1_4.html">http://www.lancache.com/pr1_4.html</a> , printed on May 20, 1997.			
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↑	Neal, D. "The Harvest Object Cache in New Zealand," <i>Computer Networks and ISDN Systems</i> , Vol. 11, No. 28, May 1996, pgs. 1415-1430.			
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↑	Network Appliance, Inc. Web Page, <a href="http://www.netapp.com/products/level3/webfiler.html">http://www.netapp.com/products/level3/webfiler.html</a> , printed on May 22, 1997.			
↑	Network Appliance, Inc. Web page, <a href="http://www.netapp.com/products/level3/nfs.html">http://www.netapp.com/products/level3/nfs.html</a> , printed on May 22, 1997.			
↑	Network Appliance, Inc. Web page, <a href="http://www.netapp.com/products/level3/ontap.html">http://www.netapp.com/products/level3/ontap.html</a> , printed on May 22, 1997.			
↑	Network Appliance, Inc. Web Page, <a href="http://www.netapp.com/products/level3/windows.html">http://www.netapp.com/products/level3/windows.html</a> , printed on May 22, 1997.			
FT	Ng, T. "The Design and Implementation of a Reliable Distributed Operating System - ROSE," <i>Proceedings of the Symposium on Reliable Distributed Systems</i> , Huntsville, October 9-11, 1990, pgs. 2-11.			
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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

FT	Patent Cooperation Treaty International Search Report, International Application No. PCT/US 97/21457, mailed on April 23, 1998, 4 pages.
11	Patent Cooperation Treaty International Search Report, International Application No. PCT/US 97/21733, mailed on April 23, 1998, 3 pages.
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	Patent Cooperation Treaty, International Search Report, International Application No. PCT/US 97/21458, mailed on April 23, 1998, 3 pages.
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	Peak Technologies, Inc. Web Page, <a href="http://www.peakJet/PeakJet.html">http://www.peakJet/PeakJet.html</a> , printed on May 20, 1997.
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	Raghavan, G. "A Domain Model of WWW Browsers," Proceeding of Southeastcon, Bringing Together Education, Science and Technology, Tampa, April 11-14, 1996, pgs. 436-439.
	Relay Web Page, <a href="http://www.relay.com/product/gold.html">http://www.relay.com/product/gold.html</a> , printed on May 22, 1997.
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FT	Sequel Technology Web Page, <a href="http://www.sequeltech.com/product/netPIM/prodinfo.html">http://www.sequeltech.com/product/netPIM/prodinfo.html</a> , printed on May 22, 1997.
	<del>Stac, Inc. Web Page, <a href="http://www.stac.com/replicat/pr-legatoagreement.html">http://www.stac.com/replicat/pr-legatoagreement.html</a>, printed on May 22, 1997.</del>

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FT	Tricord Systems, Inc. Web page, <a href="http://www.tricord.com/2/10/10-3-96.html">http://www.tricord.com/2/10/10-3-96.html</a> , printed on May 22, 1997.
FT	Wilson, Andrew W., "Hierarchical Cache/Bus Architecture for Shared Memory Multiprocessors," ACM, pp. 244-252 (1987).
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